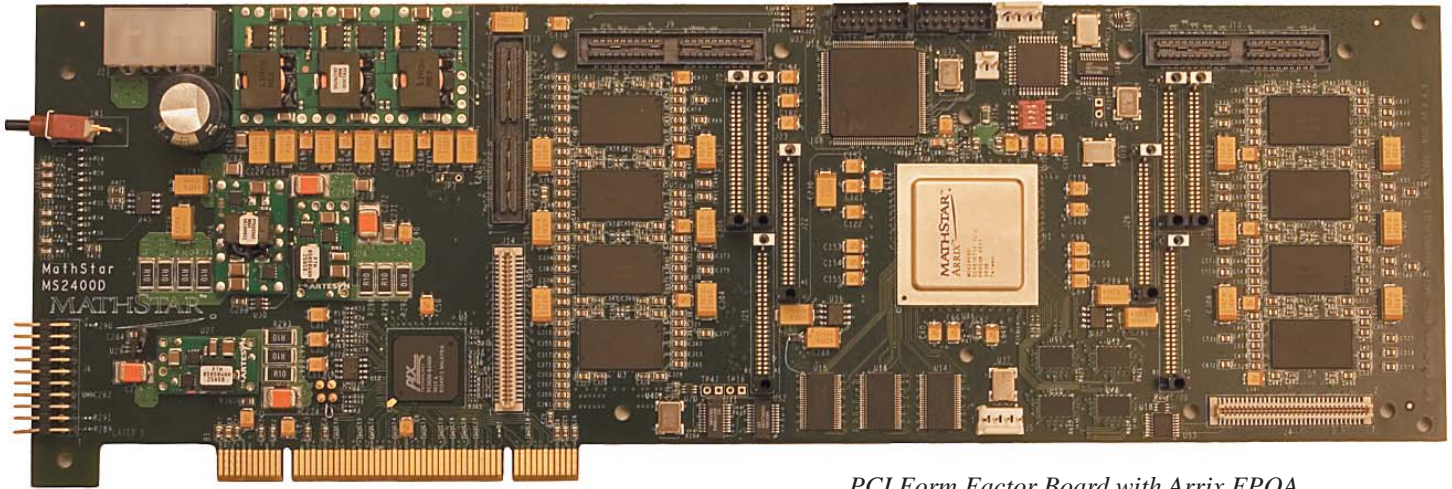




Development System Overview

The Arrix® MS2400D Development System features a high-performance Arrix Family Field Programmable Object Array (FPOA) and is designed to accelerate algorithm and system development. The development system is a PCI form factor development board integrated into a high performance PC.^[1] Applications or algorithms can be designed in MathStar Design Software (sold separately) and ported to the FPOA well in advance of designing a specific hardware platform, accelerating time-to-market and decreasing risk.



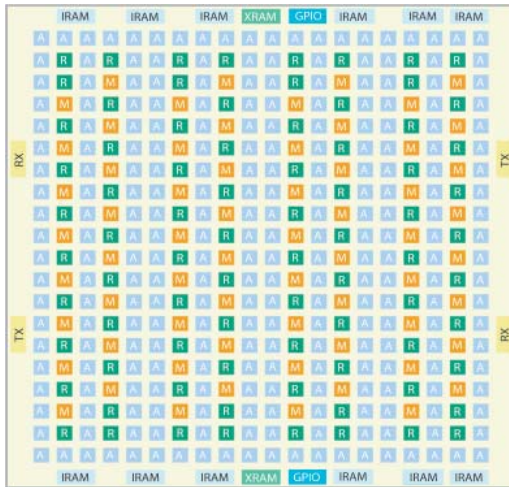
*PCI Form Factor Board with Arrix FPOA
(Ships installed in PC) [1]*

Features	Description
Arrix MOA2400D-10R FPOA	1 GHz Arrix Field Programmable Object Array on PCI ® revision 2.2a long form factor development board
Large On-board Memory	288 MB of DDR266 (4.8 Gbytes/sec total) RLLDRAM II (Reduced Latency DRAM)
Flexible Programming	Programming via PCI bus through JTAG or PROM Interfaces to the FPOA
General Purpose I/O	One external connector supporting a 48 pin (bi-directional) 100 MHz LVCMOS General Purpose I/O (GPIO) bank
High Speed I/O	Two external LVDS connectors - Each supports a 16 bit RX channel with clock and a 16 bit TX channel with clock, operating at 500 MHz DDR

13.2 GHz Intel Pentium 4, 2 GB dual-channel PC5300 DRAM (expandable to 4 GB), 1-80 GB Hard Drive, 1-DVD/CD RW Drive, Integrated video and audio, Customer Expansion slots: 1 PCI, 1 PCI-Express x1, 1 PCI-Express x16 expansion slots (after MP2400D PCI form factor board integration), 350 Watt power supply, PS2 Mouse and Keyboard, 5- USB 2.0 ports, 1000/100/10 Mbps 802.3 LAN port, Windows XP Pro. Specifications subject to change. Monitor not included.

FPOA Overview

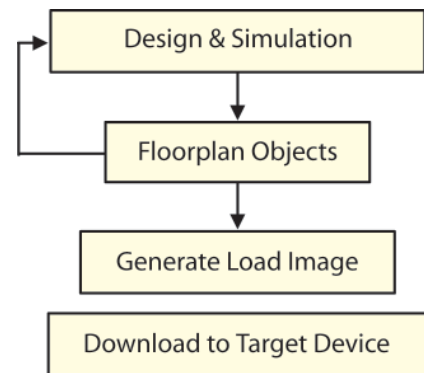
The Arrix Family of Field Programmable Objects Arrays is the second generation of FPOA products from MathStar. A 1 GHz FPOA delivers up to four times the performance of today's top FPGAs and combines high performance and re-programmability to meet a wide variety of application needs. FPOAs are comprised of hundreds of objects that pass data and signals to each other through a patented 1 GHz interconnect fabric. The Arrix Family of FPOAs support 256 Arithmetic Logic Unit (ALU), 80 Register File, and 64 MAC (multiply accumulator) objects. The objects and the interconnect fabric run on a common clock and operate deterministically at frequencies up to 1 GHz. This deterministic performance eliminates the tedious timing closure steps associated with FPGAs, reducing design iterations and development time.



- A Arithmetic Logic Units
- R Register Files
- M Multiply/Accumulators
- IRAM Internal SRAM Banks
- XRAM External Memory Interfaces
- GPIO General Purpose I/O Banks
- TX High Speed Transmit Ports
- RX High Speed Receive Ports

FPOA Design Flow

MathStar design software (sold separately) enables designers to create, verify, program and debug applications and algorithms on the Arrix FPOA. Designs are entered and simulated using Visual Elite™ design and verification environment from Mentor Graphics. Once simulated, the design is then mapped into the hardware resources of the FPOA device using the MathStar COAST physical design tool. An FPOA load image is generated and loaded onto the array via a PROM or through the JTAG interface, after which a designer can use MathStar's FPOA Debugger tool to analyze and debug the design.



Arrix Product Family Ordering

Product Description	Product Code
Arrix MS2400D Development System	MS2400D-10
Arrix FPOA Design Software	MDS-VCD02-T12FW

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