



PRESS RELEASE

Contacts:

Sean Riley
MathStar, Inc.
info@mathstar.com
503.726.5500

Jeff Hardison
McClenahan Bruer Communications
jeff@mcbru.com
503.546.1000

FOR IMMEDIATE RELEASE

MathStar, Inc. Announces the Second Generation of its Field Programmable Object Array™

Semiconductor company's Arrix™ Family of Products is up to four times faster than FPGAs

HILLSBORO, Ore., September 25, 2006 – MathStar, Inc. (Nasdaq: MATH), the FPOA leader, today announces the Arrix Family of Field Programmable Object Arrays (FPOAs). The Arrix Family is MathStar's second generation of the high-performance, re-programmable FPOA and operates at speeds up to 1-gigahertz. The Arrix Family of Products is ideally suited for performance-demanding applications in industries such as machine vision, professional video, medical imaging and military/aerospace.

The Arrix Family of Products supports 400 independent, configurable objects linked via a programmable 1-gigahertz interconnect fabric. In addition, the Arrix Family is available with two and a half times the internal memory and twice the high-speed I/O bandwidth of previous generations of the FPOA. The Arrix Family of Products also supports industrial temperature ranges and is available in both standard eutectic and RoHS-compliant packaging.

The 1-gigahertz operating frequency of the FPOA is up to four times faster than today's top FPGA architectures, depending on the application.

“The Arrix Family of FPOAs represents the best-in-class of high-performance, re-programmable logic,” said Doug Pihl, MathStar CEO. “The Arrix Family is a lot more than just a family of chips. It is also supported by FPOA design software, a PCI form-factor customer development board, and a variety of intellectual property (IP) cores.”

The Arrix Family of FPOAs is supported by IP partners in the professional video market including industry leader Barco Silex.

“Barco Silex is pleased to partner with MathStar,” said Frederic Devisch, Barco Silex

sales and marketing manager. “The combination of Barco Silex’s video IP expertise and MathStar’s high-performance FPOA architecture provides a great solution for professional video customers.”

Cadre Codesign, a leading IP developer in the machine vision market, also supports the Arrix Family of FPOAs.

“MathStar and Cadre Codesign have worked together to develop a very high-performance JPEG2000 IP core for the FPOA,” said Pierre Popovic, president and CEO of Cadre Codesign. “MathStar’s new high-performance Family of FPOA devices allows us to deliver exciting new applications to the machine vision market.”

The Arrix Family of FPOAs will be available directly from MathStar in various speed grades. The 1-gigahertz MOA2400D-10 device is priced at \$285 in 1 Ku quantities. Slower speed grades have commensurately lower pricing. For pricing and delivery options, please contact MathStar at www.mathstar.com or info@mathstar.com.

About MathStar, Inc.

MathStar is a fabless semiconductor company that designs, manufactures and markets a new class of programmable logic chips called Field Programmable Object Arrays™ (FPOAs). FPOAs are high-performance, reprogrammable integrated circuits based on proprietary Silicon Object™ technology. MathStar’s reprogrammable FPOA can process logic functions at a clock rate up to 1-gigahertz, much faster than current commercially available programmable logic devices. MathStar’s flagship product, the MOA1400D FPOA, represents a powerful solution that is ideal for digital signal processing and filtering applications in the machine vision, video processing, medical imaging and military/aerospace markets. FPOAs are available now and supported by a wide range of development tools, libraries, application notes and technical documentation. For more information, please visit www.mathstar.com.

Statements in this press release, other than historical information, may be "forward-looking" in nature within the meaning of Section 21E the Private Securities Litigation Reform Act of 1995 and are subject to various risks, uncertainties and assumptions. These statements are based on management's current expectations, estimates and projections about MathStar and its industry and include, but are not limited to, those set forth in the section of MathStar's Annual Report on Form 10-K filed with the Securities and Exchange Commission on March 31, 2006 under the heading "Risk Factors." MathStar undertakes no obligation to update any forward-looking statements in order to reflect events or circumstances that may arise after the date of this release.

###